

## Product Summary

Symbol	Value	Unit
$I_{T(RMS)}$	0.8	A
$V_{DRM} V_{RRM}$	600	V
$V_{TM}$	1.55	V

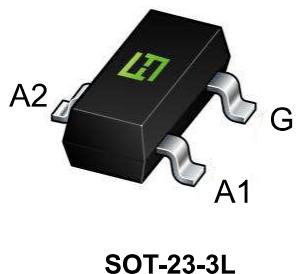
## Feature

With high ability to withstand the shock loading of large current, With high commutation performances, 4 quadrants products especially recommended for use on inductive load.

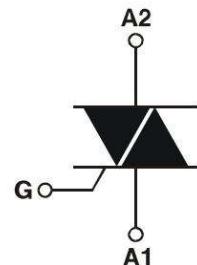
## Application

Washing machine, vacuums, massager, solid state relay, AC Motor speed regulation and so on.

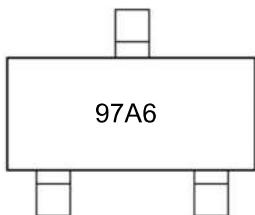
## Package



## Circuit diagram



## Marking



**Absolute maximum ratings (Ta=25°C unless otherwise noted)**

Parameter	Symbol	Value		Unit
Repetitive peak off-state voltage	V <sub>DRM</sub>	600		V
Repetitive peak reverse voltage	V <sub>RRM</sub>	600		V
RMS on-state current	I <sub>T(RMS)</sub>	0.8		A
Non repetitive surge peak on-state current (full cycle, F=50Hz)	I <sub>TSM</sub>	8		A
I <sup>2</sup> t value for fusing (tp=10ms)	I <sup>2</sup> t	0.32		A <sup>2</sup> s
Critical rate of rise of on-state current (I <sub>G</sub> =2×I <sub>GT</sub> )	dI/dt	I - II - III IV	50 10	A/μs
Peak gate current	I <sub>GM</sub>	1		A
Average gate power dissipation	P <sub>G(AV)</sub>	0.5		W
Junction Temperature	T <sub>J</sub>	-40 ~ +125		°C
Storage Temperature	T <sub>STG</sub>	-40 ~ +150		°C

**Electrical characteristics (T<sub>A</sub>=25 °C, unless otherwise noted)**

Parameter	Symbol	Test Condition		Value		Unit
Gate trigger current	I <sub>GT</sub>	V <sub>D</sub> =12V I <sub>T</sub> =0.1A T <sub>j</sub> =25°C	I - II - III	MAX.	5	mA
Gate trigger voltage	V <sub>GT</sub>		IV		7	
Gate non-trigger voltage	V <sub>GD</sub>	V <sub>D</sub> =V <sub>DRM</sub> T <sub>j</sub> =125°C		MIN.	1.2	V
latching current	I <sub>L</sub>	V <sub>D</sub> =12V I <sub>GT</sub> =0.1A T <sub>j</sub> =25°C	I - III - IV	MAX.	10	mA
			II		15	
			I - II - III - IV	MAX.	10	
Critical-rate of rise of commutation voltage	dV/dt	V <sub>D</sub> =2/3V <sub>DRM</sub> Gate Open T <sub>j</sub> =125°C		MIN.	30	V/μs
<b>STATIC CHARACTERISTICS</b>						
Forward "on" voltage	V <sub>TM</sub>	I <sub>TM</sub> =1.2A tp=380μs		MAX.	1.55	V
Repetitive Peak Off-State Current	I <sub>DRM</sub>	V <sub>D</sub> =V <sub>DRM</sub> V <sub>R</sub> =V <sub>RRM</sub>	T <sub>j</sub> =25°C	MAX.	5	μA
Repetitive Peak Reverse Current	I <sub>RRM</sub>		T <sub>j</sub> =125°C	MAX.	100	μA
<b>THERMAL RESISTANCES</b>						
Thermal resistance	R <sub>th(j-c)</sub>	Junction to case(AC)		TYP.	60	°C/W
	R <sub>th(j-a)</sub>	Junction to ambient		TYP.	150	°C/W

## Typical Characteristics

FIG.1: Maximum power dissipation versus RMS on-state current (full cycle)

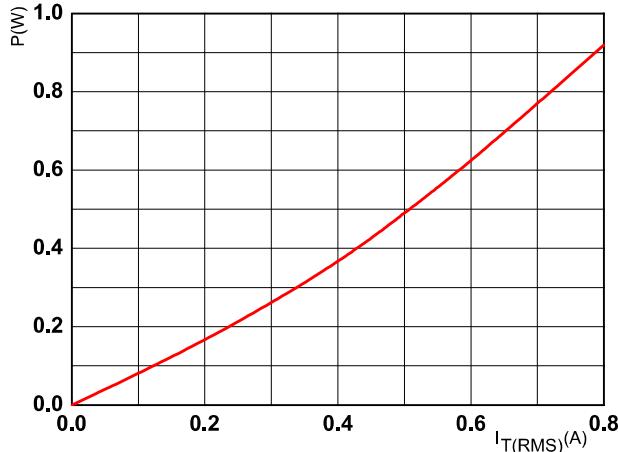


FIG.2: RMS on-state current versus case temperature (full cycle)

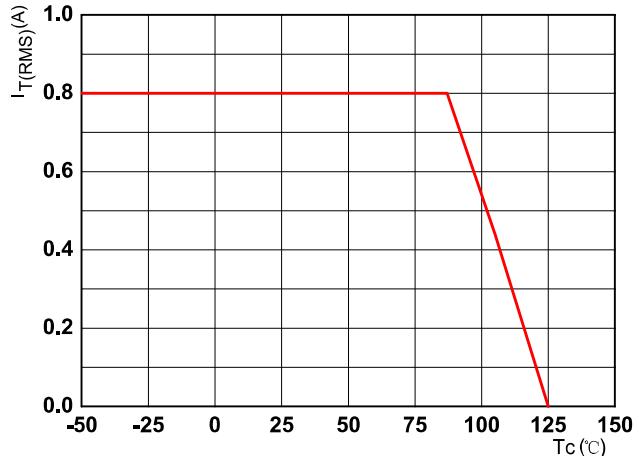


FIG.3: Surge peak on-state current versus number of cycles

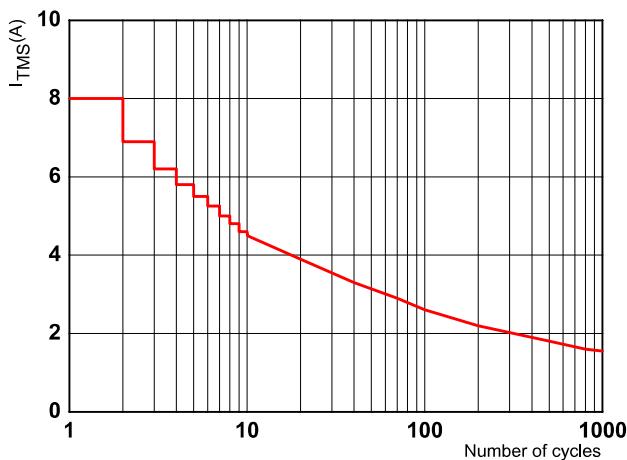


FIG.4: On-state characteristics (maximum values)

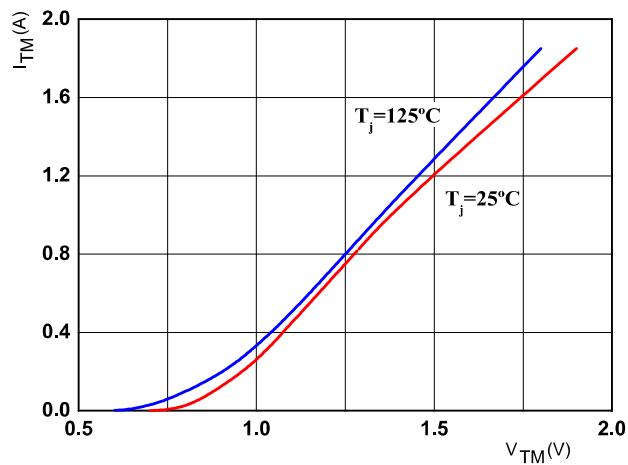


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width tp < 10ms

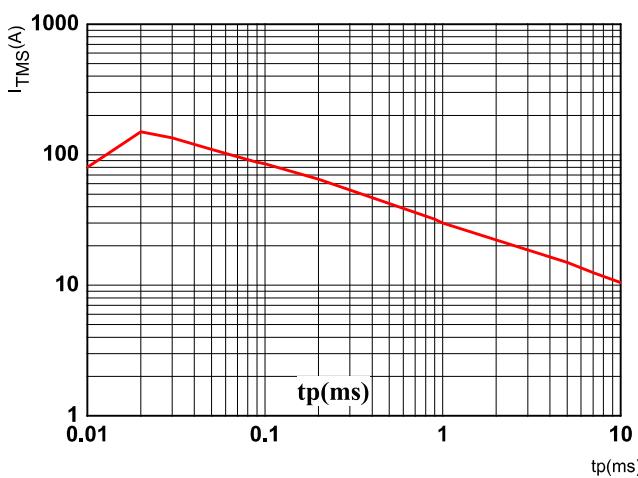
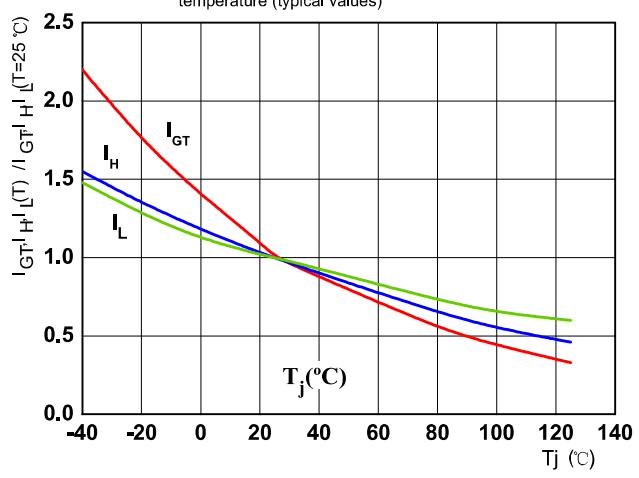


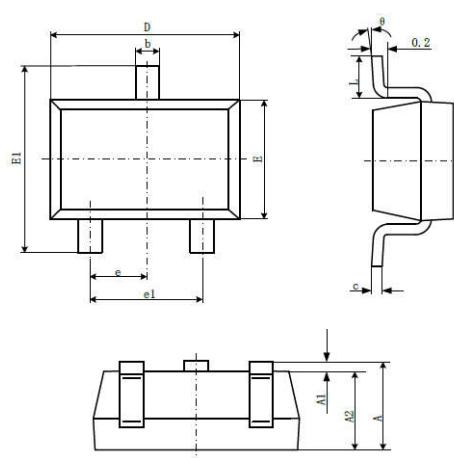
FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature (typical values)



## Ordering Information

MAC 97A 6 L  
 Triacs  
 $I_{T(RMS)}$ : 0.8A  
 L:SOT-23-3L  
 6: $V_{DRM}/V_{RRM} \geq 600V$

## SOT-23-3L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°