



AOD410

N-Channel Enhancement Mode Field Effect Transistor

General Description

The AOD410 uses advanced trench technology to provide excellent $R_{DS(ON)}$ and low gate charge. This device is suitable for use as a load switch or in PWM applications.

- -RoHS Compliant
- -Halogen Free*

Features

 $V_{DS}(V) = 30V$

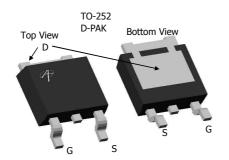
 $I_{\rm D}$ = 8A ($V_{\rm GS}$ = 10V)

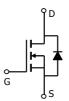
 $R_{DS(ON)}$ < 65m Ω (V_{GS} = 10V)

 $R_{DS(ON)} < 105 m\Omega (V_{GS} = 4.5 V)$

-55 to 175

100% UIS Tested! 100% Rg Tested!





Absolute Maximum Ratings T _A =25°C unless otherwise noted								
Parameter Drain-Source Voltage Gate-Source Voltage		Symbol	Maximum	Units V V				
		V_{DS}	30					
		V_{GS}	±20					
Continuous Drain	T _C =25°C		8					
Current ^G	T _C =100°C	I _D	6	A				
Pulsed Drain Current ^B		I _{DM}	20					
Avalanche Current ^C		I _{AR}	8	A				
Repetitive avalanche energy L=0.1mH ^C		E _{AR}	10	mJ				
	T _C =25°C	В	25	W				
Power Dissipation B	T _C =100°C	$-P_D$	12.5	T vv				
	T _A =25°C	В	2.1	10/				
Power Dissipation ^A	T _A =70°C	P _{DSM}	1.33	W				

Thermal Characteristics									
Parameter	Symbol	Тур	Max	Units					
Maximum Junction-to-Ambient A	t ≤ 10s R _{θJA}		20	30	°C/W				
Maximum Junction-to-Ambient A	Steady-State		46	60	°C/W				
Maximum Junction-to-Case ^C	Steady-State	$R_{\theta JL}$	5.3	7	°C/W				

 T_J, T_{STG}

Junction and Storage Temperature Range

°C

Electrical Characteristics (T_j=25°C unless otherwise noted)

Symbol	Parameter	Parameter Conditions		Тур	Max	Units
STATIC F	PARAMETERS					
BV_{DSS}	Drain-Source Breakdown Voltage	$I_D=250\mu A, V_{GS}=0V$	30			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =24V, V _{GS} =0V			1	μА
		T _J =55°C			5	μΑ
I _{GSS}	Gate-Body leakage current	V_{DS} =0V, V_{GS} = ±20V			100	nA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS} I_D=250\mu A$	1	1.8	3	V
$I_{D(ON)}$	On state drain current	V _{GS} =4.5V, V _{DS} =5V	10			Α
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =10V, I _D =8A		48	65	mΩ
		T _J =125°C		76	100	11152
		V_{GS} =4.5V, I_D =2A		75	105	mΩ
g _{FS}	Forward Transconductance	V_{DS} =5V, I_{D} =8A		6.2		S
V_{SD}	Diode Forward Voltage	I _S =1A,V _{GS} =0V		0.75	1	V
Is	Maximum Body-Diode Continuous Current				4.3	Α
DYNAMIC	PARAMETERS					
C _{iss}	Input Capacitance			288		pF
Coss	Output Capacitance	V_{GS} =0V, V_{DS} =15V, f=1MHz		57		pF
C _{rss}	Reverse Transfer Capacitance			39		pF
R_g	Gate resistance	V _{GS} =0V, V _{DS} =0V, f=1MHz		3		Ω
SWITCHI	NG PARAMETERS					
Q _g (10V)	Total Gate Charge			6.72		nC
Q _g (4.5V)	Total Gate Charge	V _{GS} =10V, V _{DS} =15V, I _D =8A		3.34		nC
Q_{gs}	Gate Source Charge	V _{GS} -10V, V _{DS} -13V, I _D -0A		0.76		nC
Q_{gd}	Gate Drain Charge			1.78		nC
$t_{D(on)}$	Turn-On DelayTime			3.7		ns
t _r	Turn-On Rise Time	V_{GS} =10V, V_{DS} =15V, R_{L} =1.8 Ω ,		3.7		ns
t _{D(off)}	Turn-Off DelayTime	R _{GEN} =3Ω		15.6		ns
t _f	Turn-Off Fall Time]		2.6		ns
t _{rr}	Body Diode Reverse Recovery Time	I _F =8A, dI/dt=100A/μs		12.6		ns
Q _{rr}	Body Diode Reverse Recovery Charge	ge I _F =8A, dI/dt=100A/μs		5.1		nC

A: The value of $R_{\theta JA}$ is measured with the device mounted on 1in 2 FR-4 board with 2oz. Copper, in a still air environment with T_A =25°C. The Power dissipation P_{DSM} is based on R $_{\theta JA}$ and the maximum allowed junction temperature of 150°C. The value in any a given application depends on the user's specific board design, and the maximum temperature for 175°C may be used if the PCB allows it.

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B. The power dissipation P_D is based on $T_{J(MAX)}$ =175°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.

C: Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}$ =175°C.

D. The R $_{\theta JA}$ is the sum of the thermal impedence from junction to case R $_{\theta JC}$ and case to ambient.

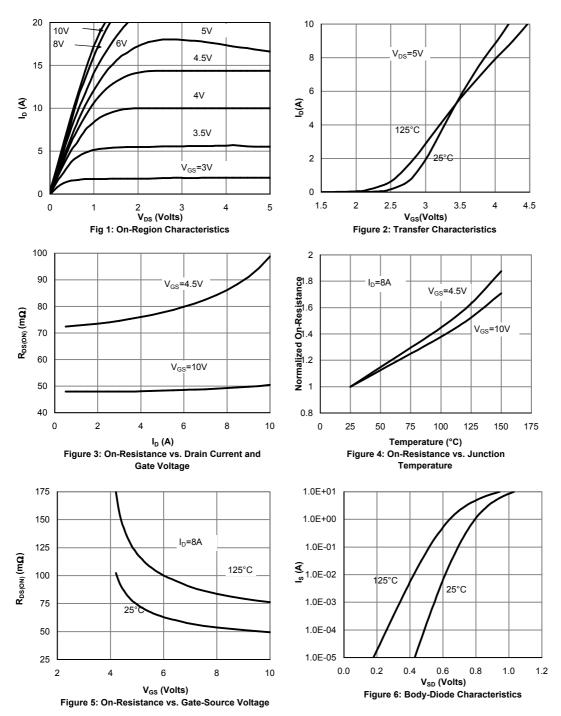
E. The static characteristics in Figures 1 to 6 are obtained using <300 μs pulses, duty cycle 0.5% max.

F. These tests are performed with the device mounted on 1 in ² FR-4 board with 2oz. Copper, in a still air environment with T_A=25°C. The SOA curve provides a single pulse rating.

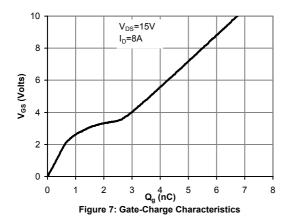
G. The maximum current rating is limited by bond-wires.

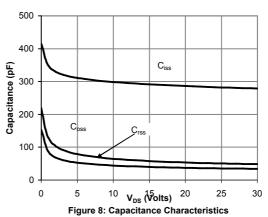
^{*}This device is guaranteed green after data code 8X11 (Sep 1ST 2008). Rev4: Oct 2008

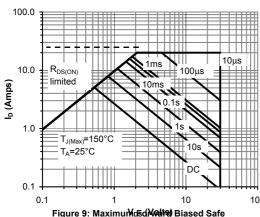
TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

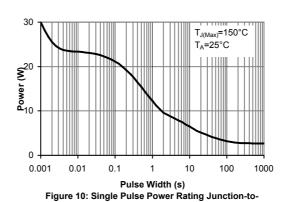


TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS









1 10 100 Ambient (Note F)
Figure 9: Maximum Area (Note F)

Operating Area (Note F)

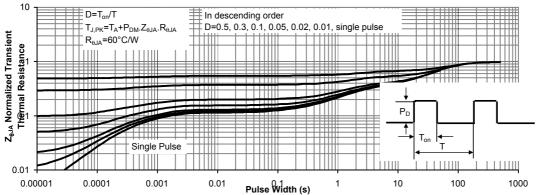
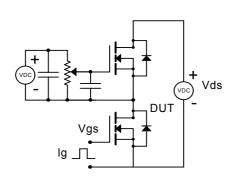
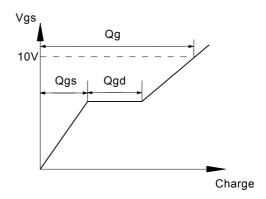


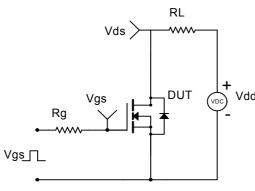
Figure 11: Normalized Maximum Transient Thermal Impedance (Note F)

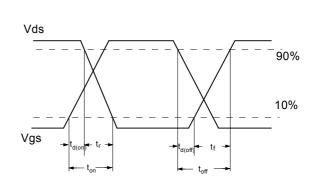
Gate Charge Test Circuit & Waveform



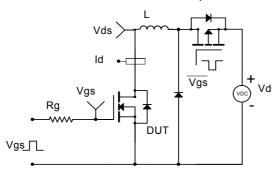


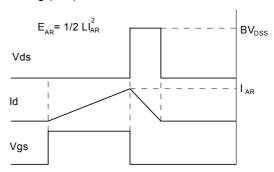
Resistive Switching Test Circuit & Waveforms





Unclamped Inductive Switching (UIS) Test Circuit & Waveforms





Diode Recovery Test Circuit & Waveforms

